Response to AEMO's Proposed Instruments for Efficient Management of System Strength Rule

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Executive Summary

On 26 April 2022, the Australian Energy Market Operator (AEMO) published an initial issue paper, titled *Amendments to AEMO instruments for Efficient Management of System Strength Rule* [1]. The Issues Paper raised 47 explicit questions. This report aims to provide a considered opinion for the following two questions:

- Question 12. Do stakeholders consider the proposed description for stable voltage waveforms to be comprehensive? Are there any recommended additions or deletions? If so, why?
- Question 14. What do stakeholders consider to be the pros and cons of the three proposed options for assessing future voltage waveform stability? Should any other options be considered? If so, what options?

Regarding Question 12, three of the four proposed criteria for stable voltage waveform are defined in terms of phasor quantities. To avoid any confusions, the relationship between voltage phasor and the voltage waveform should be clarified. For example, observing oscillations of the Root Mean Square (RMS) corresponds to sideband frequencies in the voltage waveform.

In the other criterion, the phrasing "instantaneous voltage waveform ... as close to 50 Hz sinusoidal as possible" is slightly vague. This can be improved by specifying "closeness" as a mathematical function. For example, the closeness of the waveform to a 50Hz sinusoid may be defined as the total least square error when the measured data is fitted to a pure 50Hz sinusoid. Alternatively, AEMO can specify a different mathematical function to calculate "closeness".

Regarding Question 14, assessment of future network configurations and dispatch scenarios is a balance between computational complexity and detail. Most importantly, the mechanism of instability in weak grids often involves the control systems of the grid following inverter based resources (IBR). In the proposed options, the power system is modelled by either phasor or Electromagnetic Transient (EMT) modelling. Another option is state space representation (SSR). This has the advantage of being set in the time domain, thus considers the voltage waveform unlike phasor models. Compared with EMT simulations, SSR is suited to analyse small signal and oscillatory stability using eigenvalue analysis. Thus, it would be beneficial to have a SSR of the National Electricity Market (NEM) for the purpose of system strength studies.

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Contents

1	Introduction		3
2	Crit	eria for a stable voltage waveform	4
	2.1	Item 1 – Positive Sequence RMS voltage	4
	2.2	Item 2 – Voltage Phase Angle	4
	2.3	Item 3 – Three Phase Instantaneous Voltage	5
	2.4	Item 4 – Oscillations of the RMS Voltage	5
3	Assessing Future Voltage Waveform Stability		6
	3.1	Option 1 – EMT simulation with generic model placeholders	6
	3.2	Option 2 – RMS simulation to calculate Additional Fault Levels (AFL)	6
	3.3	Option 3 – RMS simulation to calculate voltage sensitivities to power flows	7
	3.4	Another option – State Space Representation (SSR)	7
Re	References		
Appendices			9
A	A Closeness to 50Hz Sinusoid		

1 Introduction

In the context of Australia's National Electricity Market (NEM), the Australian Energy Market Commission (AEMC) updated the definition of system strength as follows [2]:

System strength relates to the stability of the voltage waveform. Along with frequency, voltage is a core electrical quality that must be maintained for a stable power system. A smooth, consistent and predictable voltage waveform is critical to the power system's voltage remaining within the parameters required for a safe transfer of energy from generators to consumers. A strong system with a stable voltage is particularly important for supporting the decarbonisation of the power sector that is currently underway.

The emphasis on "voltage waveform" is important. A potential confusion is that electrical engineers often talk about "voltage phasors". Voltage phasors are not real numbers (they are complex numbers). Thus, it must be emphasised that the AEMC working definition of system strength is *not* in terms of voltage phasors.

Instead, the definition is explicit that the time domain voltage should be considered for system strength studies (as opposed to phasor domain voltage). This is a reflection that time domain simulations are often required to accurately evaluate system strength, whereas phasor domain simulations may be insufficient on their own[3].

The significance of the voltage waveform to Inverter Based Resources (IBRs), is that the voltage waveform is an input to the control system of the IBRs. In particular, grid following IBRs estimate the phase at their point of connection and use this to set their gating signals. Hence, if the voltage waveform at the point of connection is not sinusoidal, there is a possibility that grid-following IBR may behave unexpectedly. This could result in the IBR polluting the grid with non-fundamental frequency oscillations, which may in turn disturb the voltage waveform at nearby IBRs. Thus, the rationale of strengthening the grid at locations where IBRs are connected is to avoid this type of insecurity.

The 2021 AEMC definition of system strength poses some unique practical challenges which were raised in the AEMO issue paper: How exactly does one quantify stability of a voltage waveform? How exactly does one assess voltage waveforms in practice? These correspond to Question 12 and 14 of the Issues Paper [1]. The aim of this report is to discuss the pros and cons of AEMO's proposed framework, and offer potential improvements where applicable.

2 Criteria for a stable voltage waveform

In the considered Issues Paper [1], AEMO proposes four criteria to assess the stability of voltage waveform and asks the following question:

Question 12. Do stakeholders consider the proposed description for stable voltage waveforms to be comprehensive? Are there any recommended additions or deletions? If so, why?

2.1 Item 1 – Positive Sequence RMS voltage

Since RMS voltage is a phasor quantity, it is important to consider the mapping between the actual voltage waveform and RMS phasor. An important consideration is how is the RMS value measured: is it with a True RMS meter or with a Phasor Measurement Unit or something else?

For example, suppose the RMS voltage (measured by a True RMS meter) is larger than nominal. This could arise from multiple different changes in the actual voltage waveform:

- Increasing amplitude of the 50Hz sinusoid in voltage waveform ⇒ RMS voltage will increase
- A DC offset of the voltage waveform \implies RMS voltage will increase
- A harmonic component is present in the voltage waveform \implies RMS voltage will increase

Clearly, there is not a one to one mapping between RMS voltage and voltage waveform. Hence, if using RMS voltage as a proxy for voltage waveform, if the RMS voltage shows instability, then further investigation is required to determine what is happening to the voltage waveform.

2.2 Item 2 – Voltage Phase Angle

Despite being a phasor quantity, the voltage phase angle is very relevant to the stability of grid following IBRs. This is because grid following IBRs measure the voltage waveform at the point of connection and estimate the phase angle which is subsequently used to set the gating signals. Thus, to ensure proper operation of grid following IBR, it is important that the phase angle does not change too rapidly or erratically.

The proposed requirement of 45° in 0.5 seconds, corresponds to a Rate of Change of Frequency (ROCOF) of 0.5 Hz/s. According to a 2017 report [4], 0.5Hz/s is a safe bound for the ROCOF.

2.3 Item 3 – Three Phase Instantaneous Voltage

Monitoring the instantaneous voltage is very important to assess the stability of the voltage waveform. However, the sampling frequency (T_s) should also be specified since, due to Nyquist Criterion, this limits the frequency components that can be detected.

Moreover, it is slightly vague how exactly the "closeness" to a pure 50 Hz sinusoid is evaluated. A simple regression based method is presented below.

$$v_{AB}[n] \approx a \cdot \cos\left(2\pi f_0 \cdot nT_s\right) + b \cdot \sin\left(2\pi f_0 \cdot nT_s\right)$$

$$\implies \begin{pmatrix} v_{AB}[1] \\ v_{AB}[2] \\ \vdots \\ v_{AB}[N_S] \end{pmatrix} \approx \begin{pmatrix} \cos\left(2\pi f_0 \cdot T_s\right) & \sin\left(2\pi f_0 \cdot T_s\right) \\ \cos\left(2\pi f_0 \cdot 2T_s\right) & \sin\left(2\pi f_0 \cdot 2T_s\right) \\ \vdots & \vdots \\ \cos\left(2\pi f_0 \cdot N_s T_s\right) & \sin\left(2\pi f_0 \cdot N_s T_s\right) \end{pmatrix} \begin{pmatrix} a \\ b \end{pmatrix}$$

where $f_0 = 50$ Hz is the fundamental frequency of the grid, $v_{AB}[n]$ is the measured line to line voltage at discrete time n and N_s is the number samples in a window. Mathematically, the total residuals to this regression is the "closeness" (as a mathematical norm) between the measured instantaneous voltages and a pure sinusoid. Moreover, as derived in Appendix A, this calculation can be expressed by a matrix multiplication with a closed form solution.

Alternatively, different mathematical functions can be used to calculate "closeness". Thus, to avoid ambuiguity, it would be helpful if AEMO clarified exactly what is meant by "closeness".

2.4 Item 4 – Oscillations of the RMS Voltage

As described for Item 1, when using RMS voltage, it is important to consider how this maps to the voltage waveform. For example, if 16-19Hz oscillations are observed in the RMS voltage – as has been the case in West Murray region – then what type of oscillations are occurring to the voltage waveform?

In practice, it also needs to considered, that many devices that measure RMS voltage (e.g. Phasor Measurement Units) filter the voltage waveform to extract the fundamental phasor. Thus, by design, the measured RMS voltage may leave out distortions in the actual voltage waveform.

To avoid this, if RMS measurements are being used to assess oscillatory stability, they should ideally come from True RMS meters. That way, it can be certain if there are oscillations in the voltage waveform, the True RMS voltage will also be affected.

3 Assessing Future Voltage Waveform Stability

In the considered Issues Paper [1], AEMO proposes three options for assessing future waveform stability and asks the following question:

Question 14. What do stakeholders consider to be the pros and cons of the three proposed options for assessing future voltage waveform stability? Should any other options be considered? If so, what options?

3.1 Option 1 – EMT simulation with generic model placeholders

The main advantage of this option is that the voltage waveforms are directly calculated.

However, oscillatory instability in weak grids is highly dependent on the tuning of the IBRs (i.e. gains on the PLL, inner and outer loops). Thus, a comprehensive analysis would require running EMT simulations for different tunings of the IBRs. Assuming the NEM will contain many hundreds of IBRs in the future, this means there are thousands of parameters to be varied, thus it is computationally expensive to use EMT simulation alone.

Instead, it would be preferable to utilise a less resource intensive methodology to assess the bulk of the cases and reserve EMT assessment for cases that are marginal.

3.2 Option 2 – RMS simulation to calculate Additional Fault Levels (AFL)

The main advantage of this option is that it is computationally easier to calculate fault levels than running EMT simulations. Furthermore, by considering fault levels, it gives some intepretability about the voltage stability after a disturbance.

One of the difficulties of this option is determining the minimum Short Circuit Ratio (SCR) for each IBR. Another limitation of this method it that it is not intuitive how AFL is related to the voltage stability when there is no disturbance in the network. In the West Murry Region, there have been oscillations observed even without disturbances. Thus, an AFL-based assessment might not pick up on this assessment.

Ultimately, this is a proxy method since the voltage waveform is not being directly assessed. In addition, some of the dynamics (i.e. control loops) of the system that give rise to weak grid instability have been abstracted away to reduce computational complexity.

3.3 Option 3 – RMS simulation to calculate voltage sensitivities to power flows

This option is also based on phasor model of the power system, hence it is computationally easier than running EMT simulations. Compared with AFL, it is more mathematically intuitive how the voltage phasor sensitivities to power flow contributes to instability in a weak grid, even without disturbances. One hypothesis is that nearby IBRs can interact through their outer control loops if they are both trying to control the power of the same busbar. Thus having a voltage phasor that is not sensitive to changes in power flow indicates that the grid is strong and that IBRs are effectively decoupled from each other in this region.

The main limitation of this option is that, since it considers the phasor voltage, it is a not obvious what are the possible distortions to the actual voltage waveform. Thus, it is difficult for this option to assess instabilities caused by the PLL or the inner control loop.

3.4 Another option – State Space Representation (SSR)

In addition to the options proposed by AEMO (which are based on phasor and EMT modelling), it is also worthwhile considering the State Space Representation (SSR) of the power system, which has the form:

$$\boldsymbol{x}'(t) = A\boldsymbol{x}(t) + B\boldsymbol{u}(t)$$

where x is the state vector and includes all the voltage waveforms. The matrices A and B define the dynamics of the system. The vector u contains the inputs – sinusoidal functions (from synchronous resources) and pulse trains (from IBR).

If the power system is expressed in such a SSR, then the k^{th} voltage waveform has an analytic solution of the form:

Thus, the different components (transient, sinusoidal and switching) of the waveform can be calculated. This is mathematically expedient to assess the composition of the voltage waveform. For example, the transient component becomes unstable when the modes λ_{ℓ} (which are the eigenvalues of *A* matrix have a nonnegative real part. The participation factors $\alpha_{k,\ell}$ are also useful in assessing the transient response to see which waveforms are affected by which modes.

For a particular voltage waveform, if the magnitude of the sinusoidal component is relatively large, it indicates that busbar is in the proximity of synchronous resources (e.g. synchronous machines and load centres) which are conventional sources of system strength.

Power electronic resources result in pulse trains. It is also possible to distinguish between grid following and grid forming IBR, and find the contribution of each to the voltage waveform.

Considering the insights that SSR provides about the voltage waveforms, it might be valuable to include state space studies as an addition to the system strength framework.

References

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Appendices

A Closeness to 50Hz Sinusoid

This appendix describes a mathematical method to evaluate how close measurements of an instantaneous voltage waveform are to the pure sinusoid at fundamental frequency. Let us consider the AB line to line voltage, but this method can be applied equally well to the BC line to line voltage and the CA line to line voltage. It will also work on line to neutral voltages.

Let us suppose the instantaneous voltage is measured every T_s seconds. The measurements form a discrete time signal, thus $v_{AB}[n]$ denotes the value of the voltage at time nT_s seconds. Now, if the instantaneous voltage was a pure sinusoid at fundamental frequency f_0 the following equation will hold for all values of n:

$$v_{AB}[n] = a \cdot \cos\left(2\pi f_0 \cdot nT_s\right) + b \cdot \sin\left(2\pi f_0 \cdot nT_s\right)$$

where the scalars $a, b \in \mathbb{R}$ are coefficients that determine the amplitude $(\sqrt{a^2 + b^2})$ and phase angle $(\operatorname{atan2}(b, a))$ of the sinusoid. We do a regression to estimate a, b, thus the total residual will tell us how "close" (as a mathematical norm) the measured instantaneous voltage is to the pure sinusoid.

Let us consider a window of N_s measurements. Thus, we have a linear system of N_s equations which can conveniently be expressed in matrix notation as follows:

$$\underbrace{\begin{pmatrix} v_{AB}[1] \\ v_{AB}[2] \\ \vdots \\ v_{AB}[N_S] \end{pmatrix}}_{v} = \underbrace{\begin{pmatrix} \cos\left(2\pi f_0 \cdot T_s\right) & \sin\left(2\pi f_0 \cdot T_s\right) \\ \cos\left(2\pi f_0 \cdot 2T_s\right) & \sin\left(2\pi f_0 \cdot 2T_s\right) \\ \vdots & \vdots \\ \cos\left(2\pi f_0 \cdot N_s T_s\right) & \sin\left(2\pi f_0 \cdot N_s T_s\right) \end{pmatrix}}_{M} \begin{pmatrix} a \\ b \end{pmatrix}$$

Note that *M* is $N_s \times 2$ matrix that is constant. In order to solve this in a least squares sense we need to evaluate:

$$\begin{pmatrix} a \\ b \end{pmatrix} = \left(M^{\mathrm{T}} M \right)^{-1} M^{\mathrm{T}} \boldsymbol{v}$$

where M^{T} is the transpose of M. As such, $M^{T}M$ is a 2 × 2 matrix which is simple to invert. Finally, the residuals are calculated as:

$$oldsymbol{d} = \left(M\left(M^{\mathrm{T}}M
ight)^{-1}M^{\mathrm{T}}
ight)oldsymbol{v} - oldsymbol{v}$$

and the squared error is simply $\varepsilon = d^{T}d$. So ε can be used to quantify how close the voltage waveform is to a pure sinusoid.

The complete algorithm to find ε is given below:

Algorithm 1 Closeness of voltage waveform to pure sinusoid			
Define constant matrix M			
Define constant matrix $S \leftarrow M (M^{T}M)^{-1} M^{T} - I$			
LOOP:			
$oldsymbol{v} \leftarrow N_s$ most recent voltage measurement			
$d \leftarrow Sv$ // $2N_s^2 - N_s$ FLOP			
$\varepsilon \leftarrow d^{\mathrm{T}}d \qquad //2N_{\mathrm{s}} - 1 \mathrm{FLOP}$			
Wait T_s seconds for new measurement			
GOTO LOOP			

Note that *I* corresponds to a $N_s \times N_s$ identity matrix.

Within the loop, we are performing $2N_s^2 + N_s - 1$ floating point operations. For example, if we are considering a window of $N_s = 20$ measurements and a sampling interval of $T_S = 1$ millisecond, then that means each loop we need to perform 819 floating point operations, implying the processor needs to be capable of 819000 floating point operations per second (FLOPS). Modern processors are capable of billions of FLOPS thus it is clear that the proposed algorithm is computationally feasible.